(54) Title: SYNCHRONIZING OF A DIGITAL SIGNAL USING A PCR PROGRAM CLOCK REFERENCE
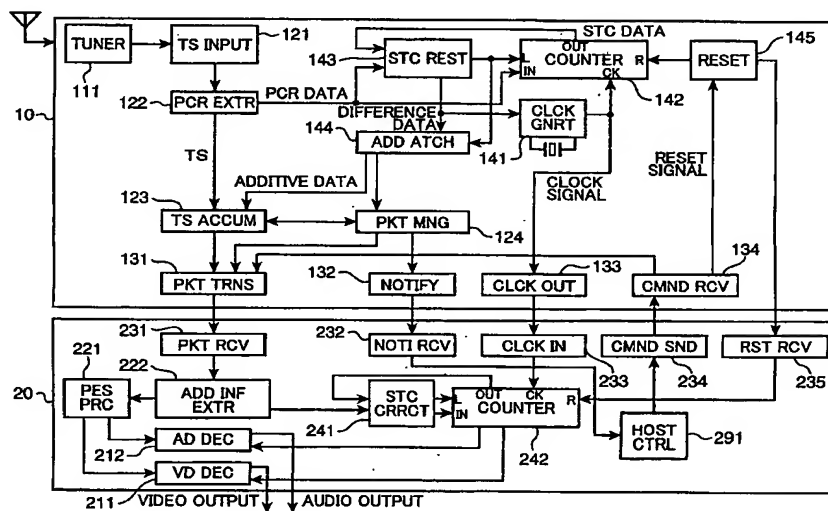
(57) Abstract: A digital signal receiving system is provided for establishing high-speed clock resynchronization even if abnormality
such as a poor radio wave receiving condition takes place. The system is constructed such that recipient STC data and PCR data are
acquired in response to detection of a variation in frequency of a clock signal, and these data are stored as variation information data.
The receiver 10 sets the PCR data in the R_STC counter 142, and sends the variation information data to the host device 20. The
host device 20, in turn, sets a computation result obtained based on host STC data and the variation information data in the H_STC
counter 242.

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.